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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/847,667	05/01/2001	Subhash Gupta	54364	4777	
75	90 09/07/2004		EXAMI	NER	
The Law Offices of Calvin B. Ward			MITCHELL, JAMES M		
Suite 305 18 Crow Canyo	n Court		ART UNIT PAPER NUMBER		
San Ramon, CA 94583			2813		
			DATE MAILED: 09/07/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	
Advisory Action	09/847,667	GUPTA ET AL.	
riarioti y riolion	Examiner	Art Unit	
	James M. Mitchell	2827	
The MAILING DATE of this communication appe	ears on the cover sheet with the d	correspondence add	iress
THE REPLY FILED 19 August 2004 FAILS TO PLACE Therefore, further action by the applicant is required to a final rejection under 37 CFR 1.113 may only be either: (condition for allowance; (2) a timely filed Notice of Appearance (RCE) in compliance with 37 CFR 1.114.	void abandonment of this application to the second	cation. A proper reich places the appli	ply to a cation in
PERIOD FOR RE	EPLY [check either a) or b)]		
a) The period for reply expiresmonths from the mailing of the period for reply expires on: (1) the mailing date of this Adverse, however, will the statutory period for reply expire later the ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f).  Extensions of time may be obtained under 37 CFR 1.136(a). The dath have been filed is the date for purposes of determining the period of extensions of the calculated from: (1) the expiration date of the shortened (b) above, if checked. Any reply received by the Office later than three more earned patent term adjustment. See 37 CFR 1.704(b).	risory Action, or (2) the date set forth in the an SIX MONTHS from the mailing date of FILED WITHIN TWO MONTHS OF THE te on which the petition under 37 CFR 1.1 sion and the corresponding amount of the statutory period for reply originally set in	f the final rejection. E FINAL REJECTION. S  136(a) and the appropriate ex the final Office action; or	See MPEP  te extension fee  tension fee under  (2) as set forth in
1. A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CF	R 1.191(d)), to avoid dismissal		
2. The proposed amendment(s) will not be entered be	ecause:		
(a) they raise new issues that would require furth	er consideration and/or search (	(see NOTE below);	
(b) ☐ they raise the issue of new matter (see Note to	pelow);		
<ul><li>(c)  they are not deemed to place the application issues for appeal; and/or</li></ul>	in better form for appeal by mat	erially reducing or	simplifying the
(d) they present additional claims without cancel	ing a corresponding number of	finally rejected clai	ms.
NOTE:			
3. Applicant's reply has overcome the following reject	tion(s):		
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a s	separate, timely file	d amendment
5.☐ The a)☐ affidavit, b)☐ exhibit, or c)☐ request fo application in condition for allowance because:		sidered but does No	OT place the
6. The affidavit or exhibit will NOT be considered becaused by the Examiner in the final rejection.	cause it is not directed SOLELY	to issues which we	ere newly
7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we	• • •		and an
The status of the claim(s) is (or will be) as follows:			
Claim(s) allowed:			
Claim(s) objected to:			
Claim(s) rejected: 1-10 and 17.			
Claim(s) withdrawn from consideration:			
8. The drawing correction filed on is a) app	roved or b) disapproved by	the Examiner.	
9. Note the attached Information Disclosure Stateme	nt(s)( PTO-1449) Paper No(s).	<u> </u>	
10. Other: See Continuation Sheet			
		1/11	
= Attho	EF PRIMA	RIK KIELIN RY EXAMINER	

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PTOL-303 (Rev. 11-03)

Continuation of 10. Other: The drawing objection is maintained. While applicant is free to to show elements as a labeled representation where their detailed illustration is not essential for a proper understanding of the invention, in this case, layer 22 illustrated in figure 1 on a first surface of a substrate, 21, deters from the proper understanding of the invention, which includes circuit layers being formed on and extending into the substrate. Thus while each IC does need to be shown, the positonal relationship of layer 22 with regard to the substrate needs to be shown with layer 22 protruding into the substrate. Likewise, the 112 rejection is maintained. While applicant contends that the feature is well known, and that it is not aware of any patent law that requires applicant to teach what is known in the art, applicant is directed to M.P.E.P 2163 [R-2] that to satsfy the written description, a patent "must describe the claimed invention in sufficient detail that one of ordinary skill in the art can reasonably conclude that the inventor had possession of the claimed invention." Simply because a layer of conventional components is shown drawn on a substrate's top surface in applicant's figure 1, that does not eliminate the requirement that one of ordinary skill in the art be able to conclude that inventor had possession of what it claimed. Since the claimed feature of an IC extending in the substrate is not an inherent characteristic, it is deemed unreasonable to assume that possession can be concluded by such a drawing or a mere statement that the layers "are constructed using conventional fabrication techchniques." The requirement of possession is not waived, because part of applicant's invention includes something that is widely known (i.e. the unobvious combination of old parts can create a new invention).